

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	3104	GPR or "general purpose registers"	USPA T	2004/05/28 10:09	
2	BRS	L2	285	l1 with operand	USPA T	2004/05/28 10:10	
3	BRS	L3	50	l2 same (ALU or "arithmetic logic unit")	USPA T	2004/05/28 10:10	
4	BRS	L4	1	l3 same (encrypt\$3 or encipher\$3 or encod\$3)	USPA T	2004/05/28 10:32	
5	BRS	L5	11	l3 and (encrypt\$3 or encipher\$3 or encod\$3)	USPA T	2004/05/28 10:33	
6	BRS	L6	2	l3 and permut\$6	USPA T	2004/05/28 10:51	
7	BRS	L7	0	l2 same permut\$6	USPA T	2004/05/28 10:51	
8	BRS	L8	7	l2 and permut\$6	USPA T	2004/05/28 10:54	
9	BRS	L9	0	l2 and (s-box or p-box)	USPA T	2004/05/28 10:54	
10	BRS	L10	0	l2 and (sbox or pbox)	USPA T	2004/05/28 10:54	
11	BRS	L11	0	l2 and (data adj encryption)	USPA T	2004/05/28 10:55	
12	BRS	L12	1	l2 and ("des")	USPA T	2004/05/28 10:58	
13	BRS	L13	1106	"des" near5 algorithm	USPA T	2004/05/28 10:58	
14	BRS	L14	0	l1 same l13	USPA T	2004/05/28 10:59	
15	BRS	L15	5	l1 and l13	USPA T	2004/05/28 11:14	
16	BRS	L16	1719	"data encryption standard"	USPA T	2004/05/28 11:15	
17	BRS	L17	2	l16 same (alu or "arithmetic logic unit")	USPA T	2004/05/28 11:18	
18	BRS	L18	4	l16 same (s-box or p-box)	USPA T	2004/05/28 11:19	

	Error Definition	Er ro rs
1		0
2		0
3		0
4		0
5		0
6		0
7		0
8		0
9		0
10		0
11		0
12		0
13		0
14		0
15		0
16		0
17		0
18		0